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Amendment to the Specification:

Please replace the title of the invention with the following amended title:

IMAGE SENSOR HAVING A REDUCED NUMBER OF WHITE
PIXELS AND A REDUCED FIXED PATTERN NOISE

Please substitute the following amended paragraph for the pending paragraph beginning on page 7, line 31.

A timing diagram for the operation of the three-transistor cell 4 is shown in FIG. 3. In typical operation a node N1 is set to a predetermined Voltage Vdd' (which may be different from the circuit operating voltage Vdd) by turning on an n-channel reset transistor 6. The state of the reset transistor is determined by controlling a reset voltage (Vreset). In FIG. 3, Vreset goes high at time T0, causing the node N1 to ramp to Vdd'. At time T1, the reset transistor 6 is turned off and photoelectrons are generated by the incident light on a photosensitive element in the form of a photodiode 5. The photoelectrons are injected into node N1, reducing the voltage on that node by a value of $V_{sense} = Vdd' - (I_{photo} \times T_{illuminate} / C_{N1})$. In this equation I_{photo} is the photocurrent induced by the incident light, $T_{illuminate}$ is the illumination time period and $C_{sub.N1}$ is the capacitance on node N1. Both Vdd' and Vsense can in principle be read out of the pixel by a source follower transistor 16 with a gate 17 by activating a row-select transistor 25. In a two-dimensional array of cells, there typically are row-select transistors and column-select transistors that allow the cells to be sequentially sampled. The row select transistor 25 is activated by manipulating a row-select signal. The illumination on the cell is then proportional to $Vdd' - V_{sense} = I_{photo} \times T_{illuminate} / C_{N1}$. Persons skilled in the art refer to this operation as Double Sampling. Sampling occurs at time T2 before $T_{illuminate}$ and time T3 during $T_{illuminate}$. The cell is reset at time T4, since Vreset is caused to go high.

Please add the following new paragraph at page 13, line 26.

The invention has been described with reference to the preferred embodiments. Modifications and alterations will occur to others upon a reading and understanding of the preceding detailed description. It is intended that the invention

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be construed as including all such modifications and alterations insofar as they come within the scope of the appended claims or the equivalents thereof.